

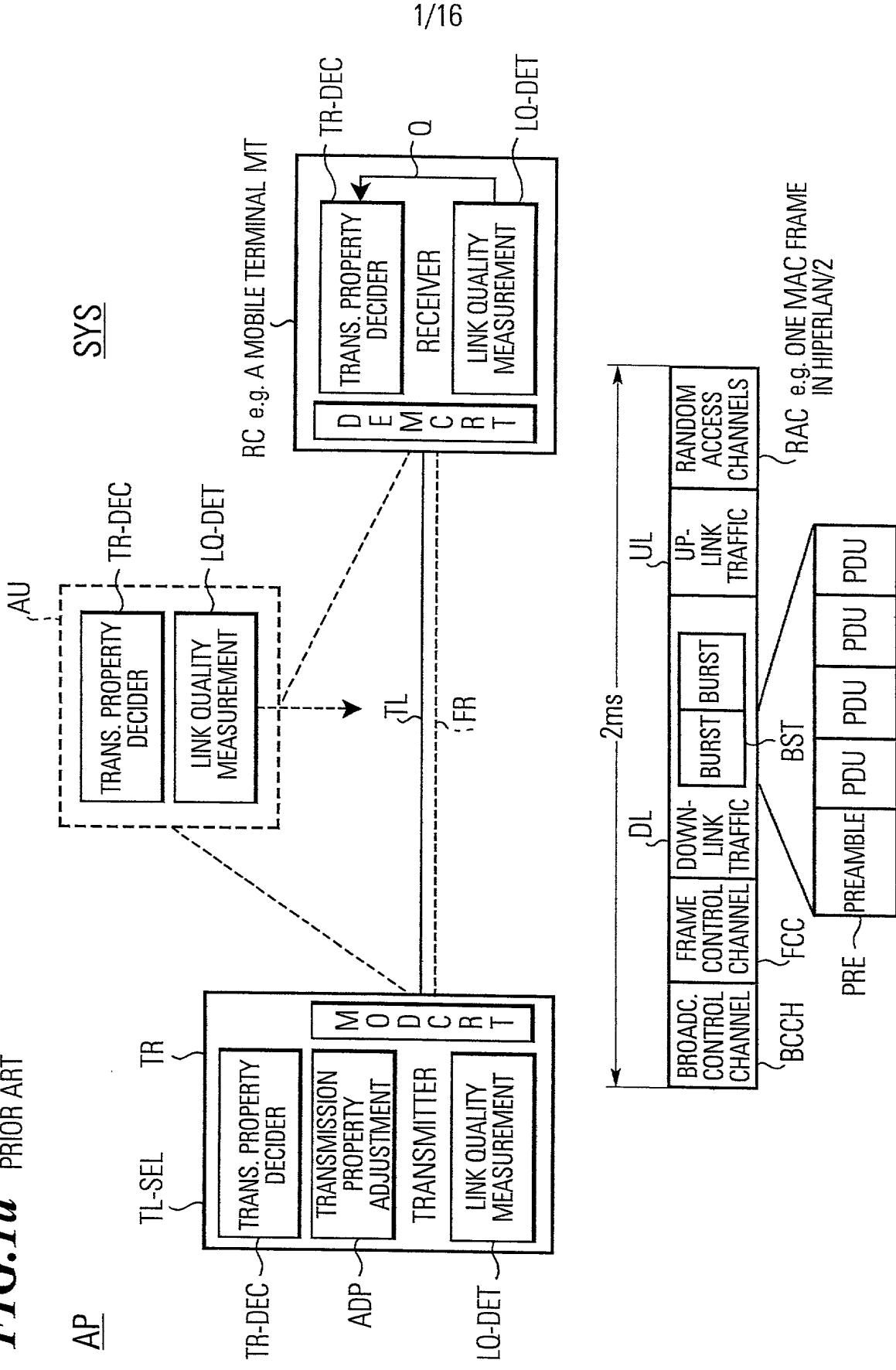
FIG.1a PRIOR ART

FIG.2a

PRIOR ART

MODE	MODULATION	CODE RATE	PHYSICAL LAYER BIT RATE
1	BPSK	1/2	6 Mbps
2	BPSK	3/4	9 Mbps
3	QPSK	1/2	12 Mbps
4	QPSK	3/4	18 Mbps
5	16 QAM	9/16	27 Mbps
6	16 QAM	3/4	36 Mbps
7	64 QAM	3/4	54 Mbps

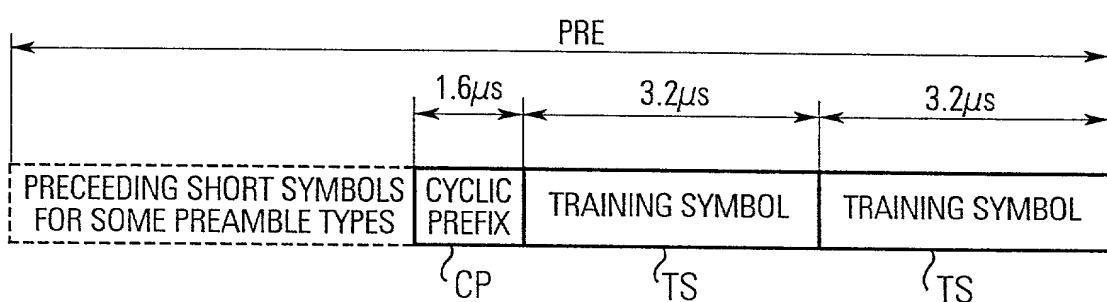
FIG.2b

PRIOR ART

PARAMETER	SYMBOL	VALUE IN HIPERLAN/2
CARRIER FREQUENCY		5.2-5.8 GHz
CHANNEL FREQUENCY SPACING		20 MHz
SAMPLING RATE AFTER 64-POINT IFFT	1/T	20 MHz (= 1/(50ns))
FFT LENGTH		64
NUMBER OF USED SUBCARRIERS	N_{ST}	52
NUMBER OF DATA SUBCARRIERS	N_{SD}	48
NUMBER OF PILOT SUBCARRIERS	N_{SP}	4
LENGTH OF CYCLIC PREFIX		16 SAMPLES, EQ. TO $0.8\mu s$
OFDM SYMBOL PERIOD INCL. CYCLIC PREFIX		$4\mu s$

FIG.1b

PRIOR ART

**FIG.3**

PRIOR ART

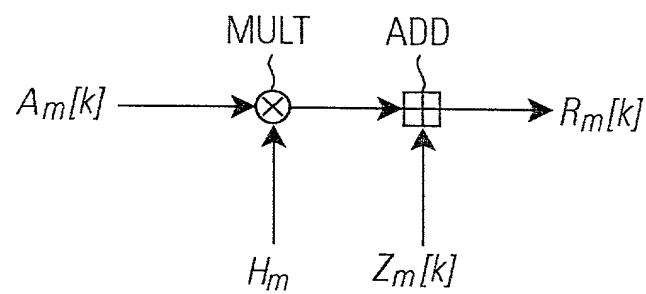


FIG. 4a PRINCIPLE OF THE INVENTION

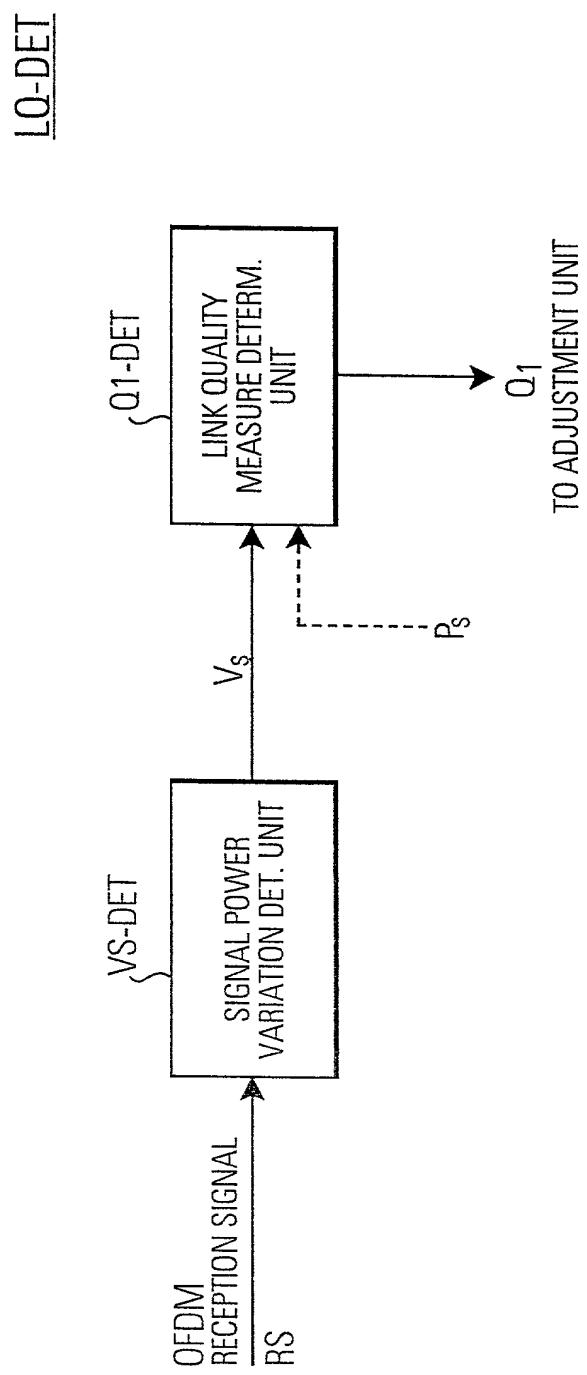


FIG. 4b

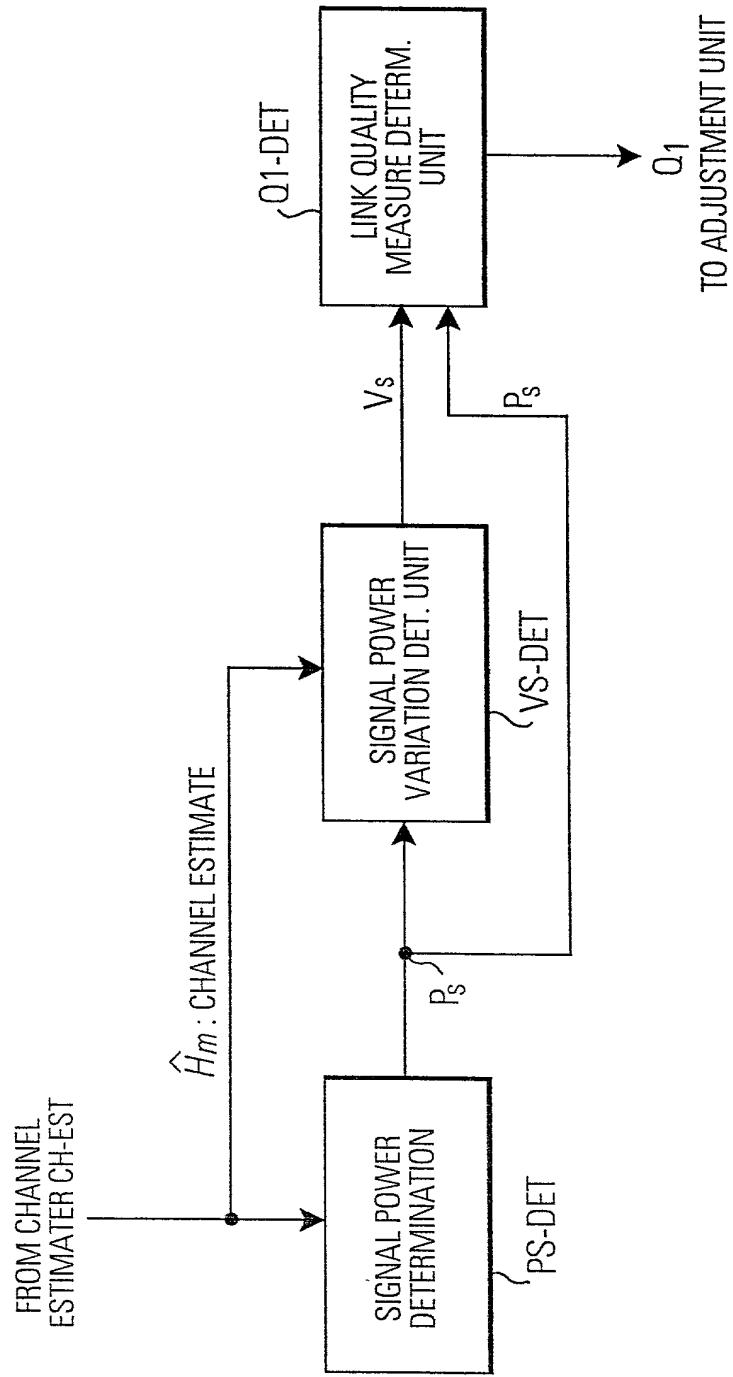


FIG. 5a PRINCIPLE OF THE INVENTION

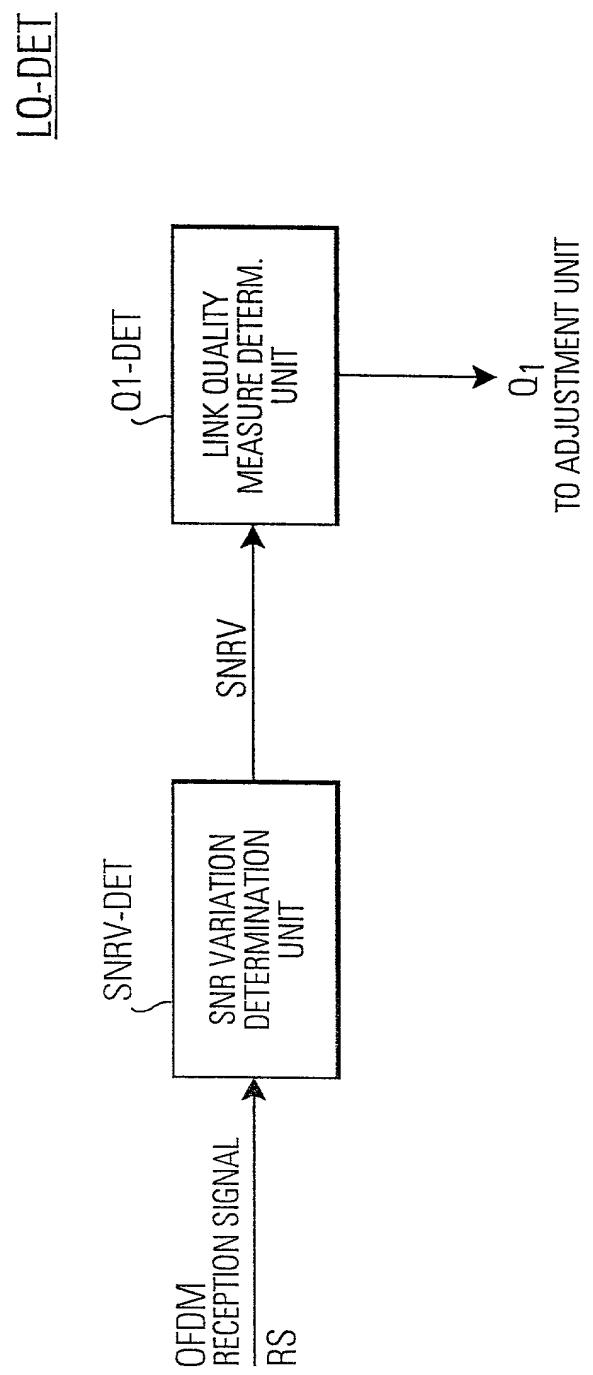


FIG.5b

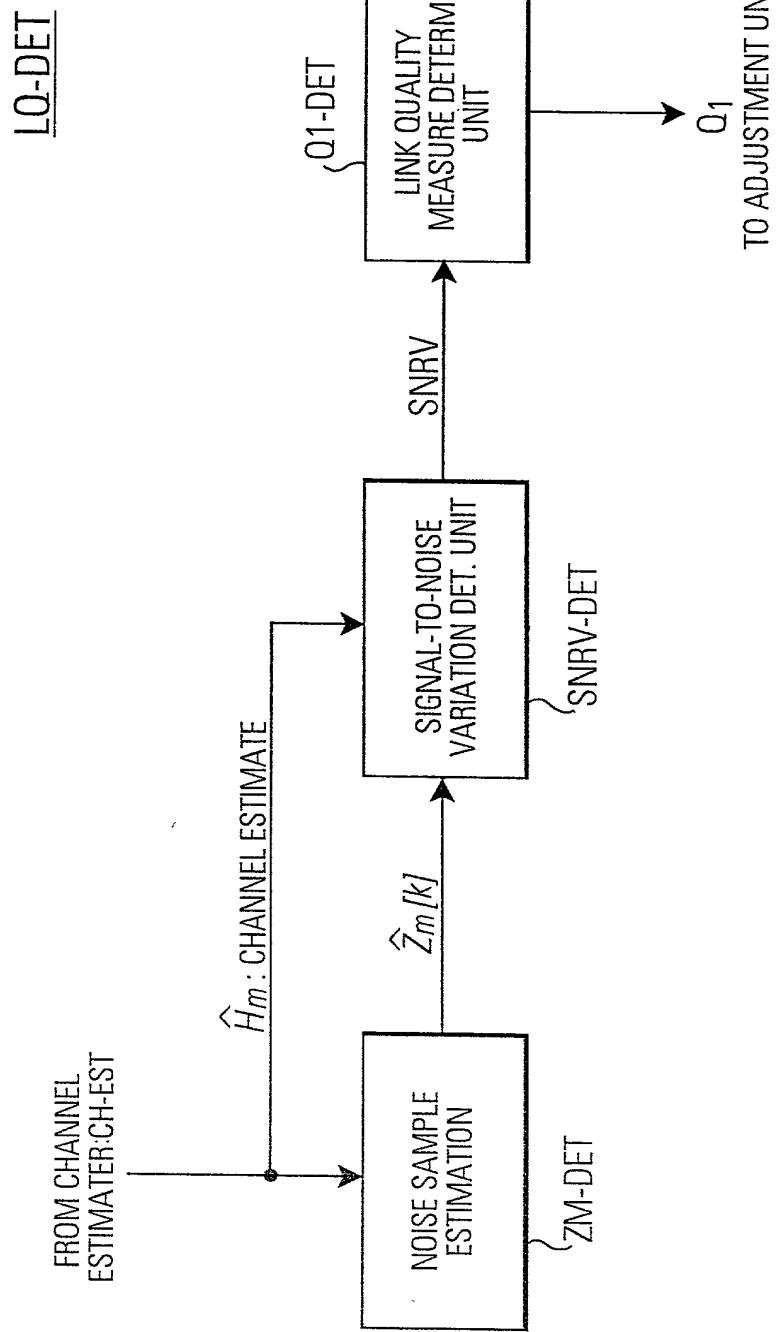


FIG.6

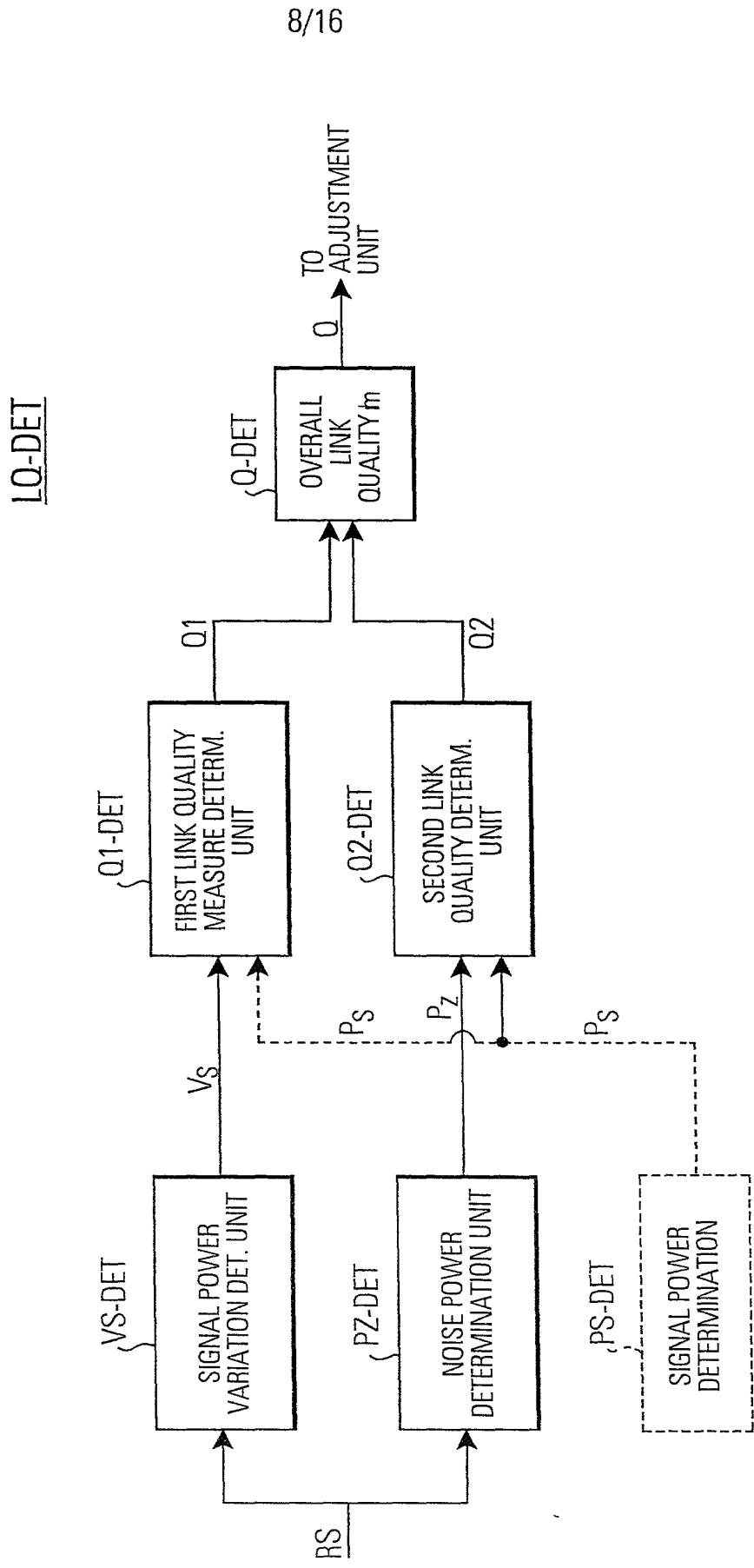


FIG. 7

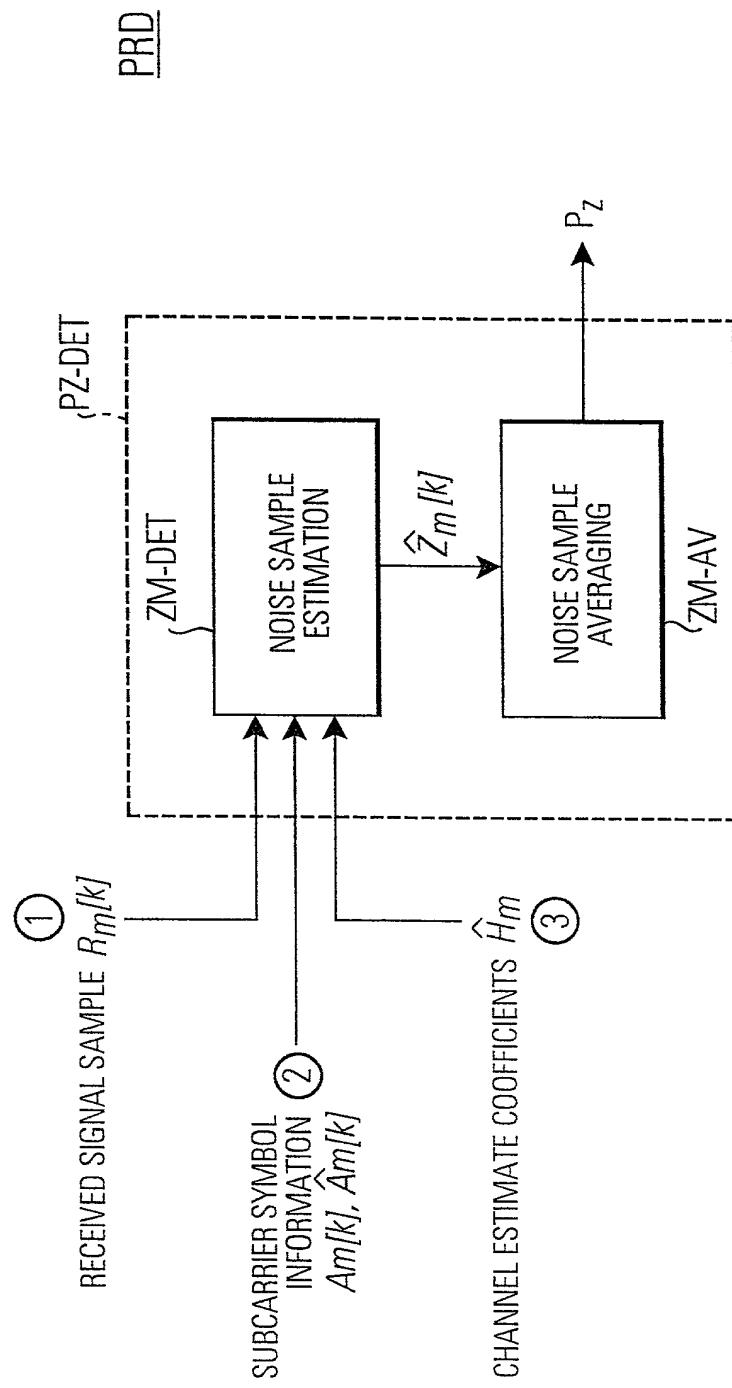


FIG. 8

DEMOD-CRT

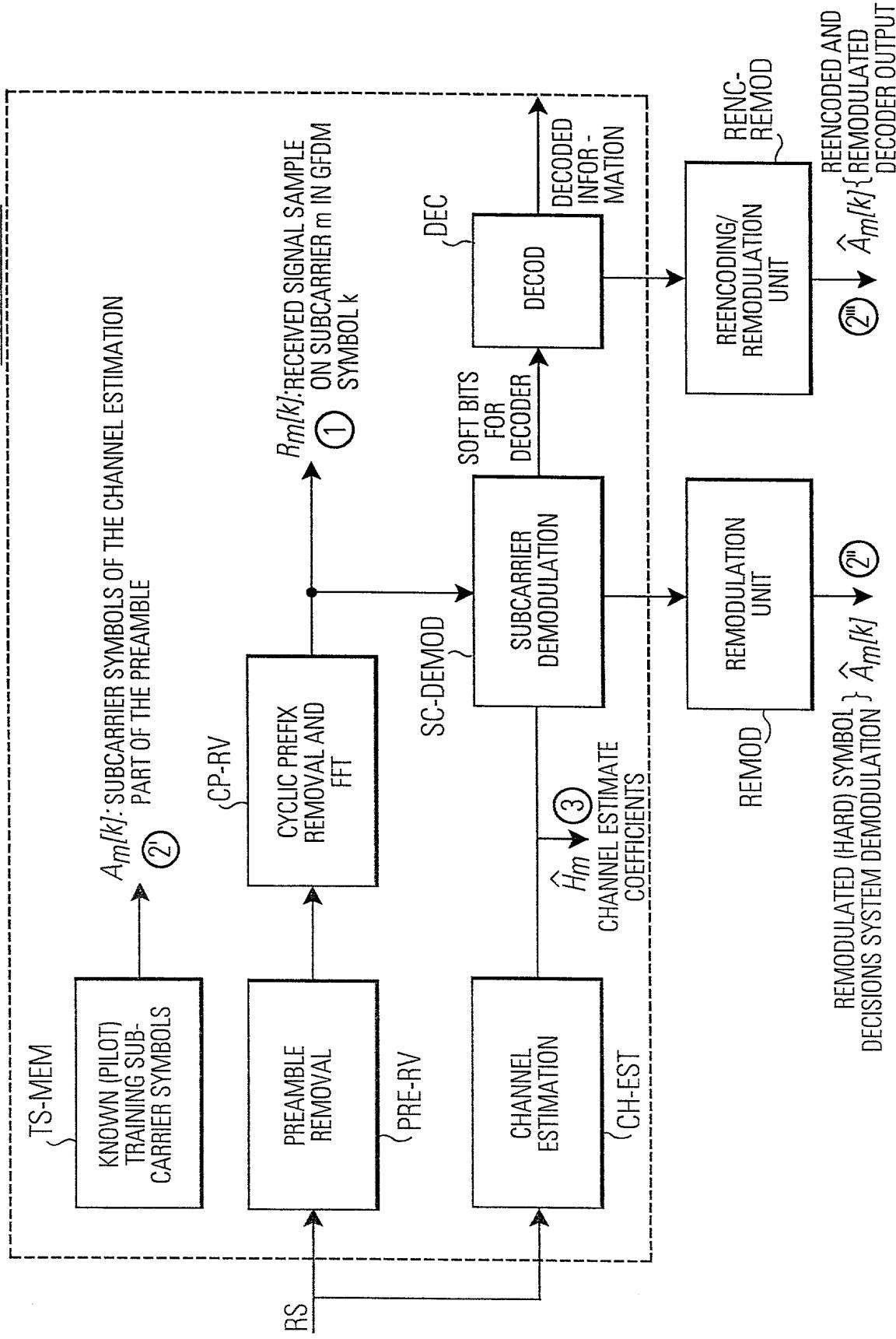


FIG.9

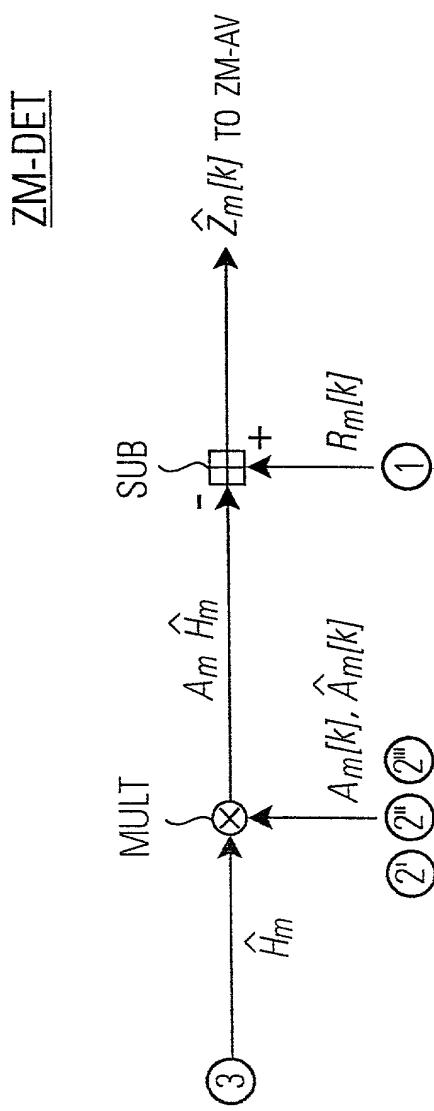


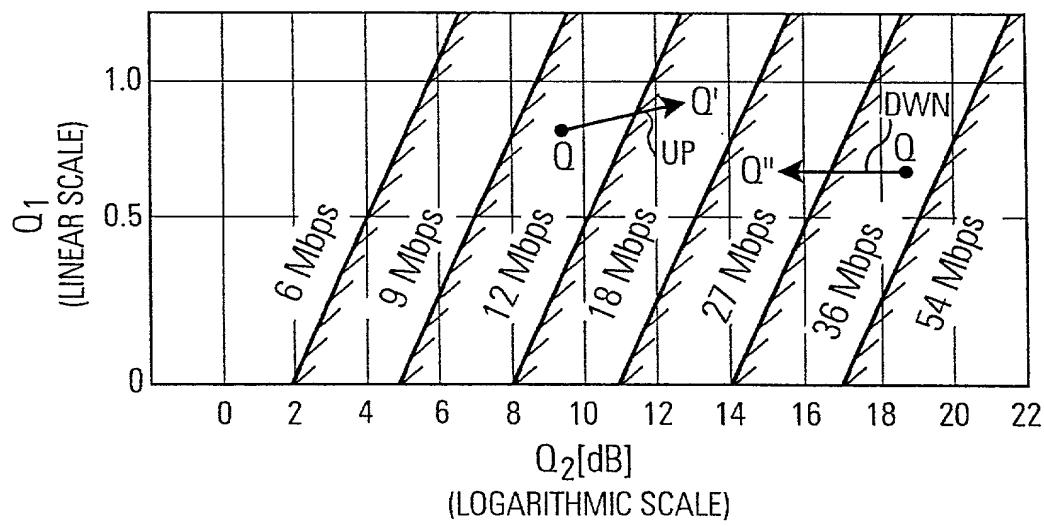
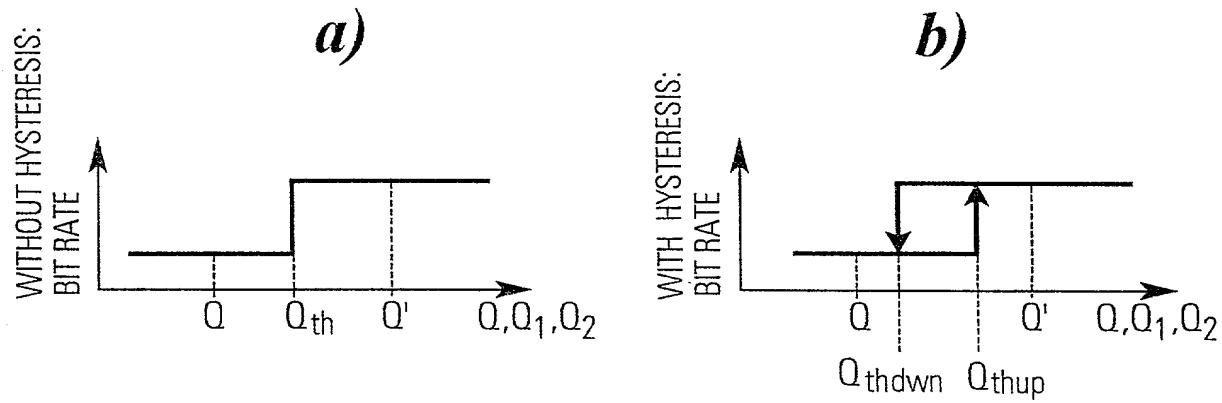
FIG.10**FIG.11**

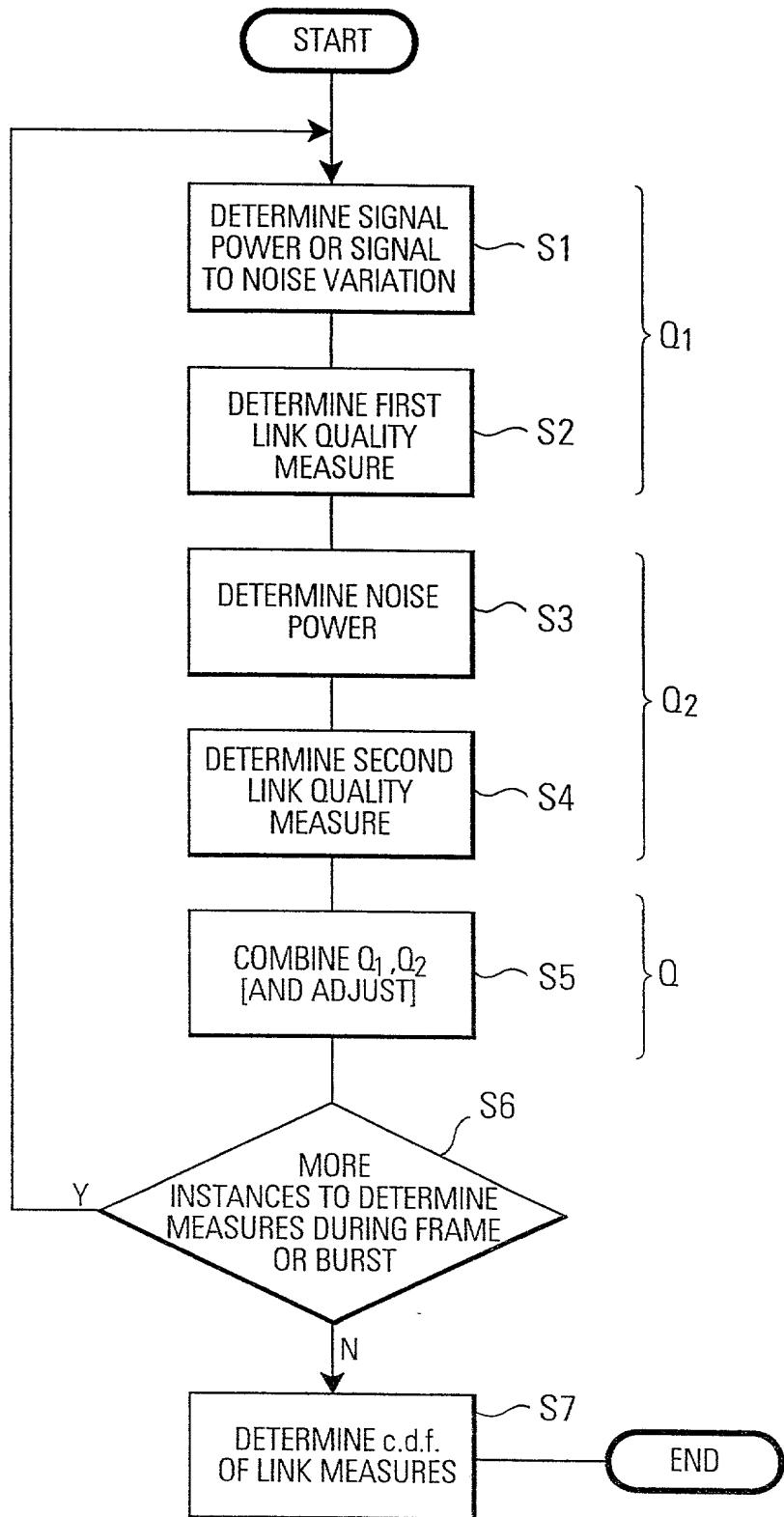
FIG.12

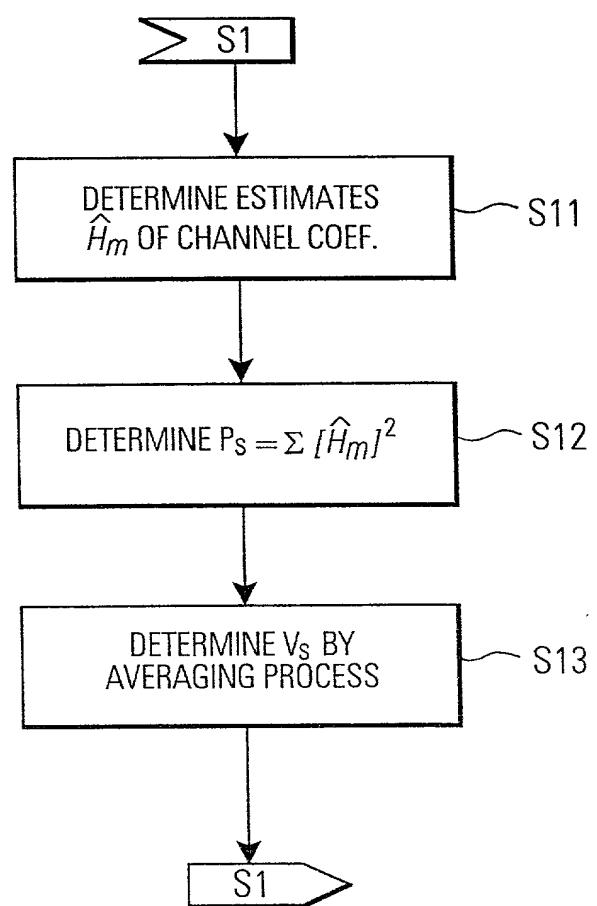
FIG.13a

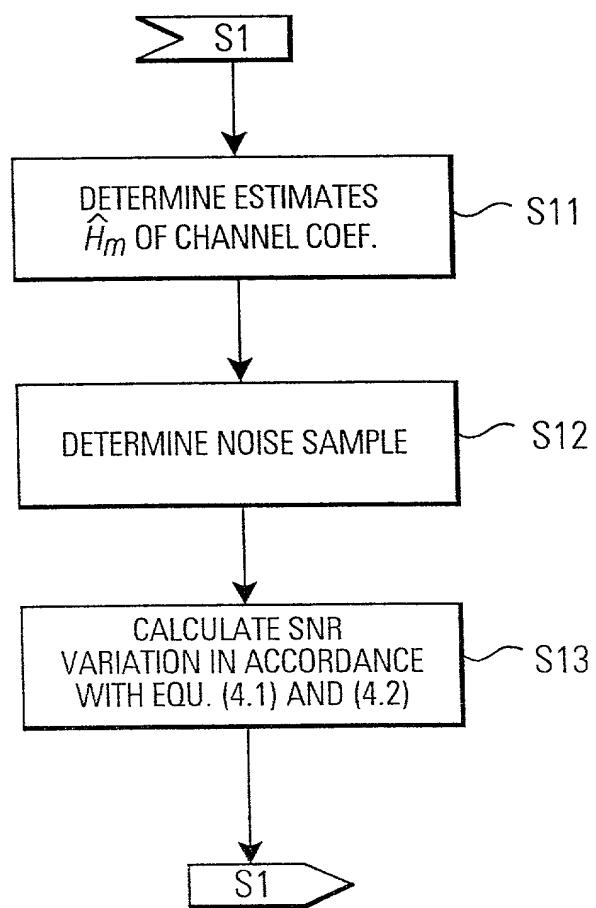
FIG.13b

FIG.13c